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Hayes13DV-13190
PATENT

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant: Cook et al.

Serial No.: 09/552,563—

Filed: March 10, 2000

For: CONTROL SYSTEMS AND
METHODS: Art Unit: 2858
: Examiner: Patel, P.
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AMENDMENT

Assistant Commissioner for Patents
Box NON-FEE AMENDMENT
Washington, D.C. 20231

Sir:

In response to the Office Action dated October 25, 2001, please amend the above-identified patent application as follows:

IN THE CLAIMS

1. (once amended) A method for detecting faults in a transducer including a secondary winding having at least two voltage outputs, the transducer being electrically connected to a logic circuit, said method comprising the steps of:

summing the voltage outputs to obtain a summed voltage value;

determining a difference between a current value of the summed voltage value to a reference value; and